

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Tsukasa Yajima

Group Art Unit: Not Assigned

Division of Serial No.: 09/038,749

Examiner: Not Assigned

Filed: January 25, 2001

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE (To be Amended)

DRAWING CORRECTION APPROVAL REQUEST

Honorable Assistant Commissioner
of Patents and Trademarks,
Washington, DC 20231

Date: January 25, 2001

Sir:

Applicant respectfully requests the Examiner's approval of the following drawing corrections indicated in red ink on the attached sheets. The following are the corrections.

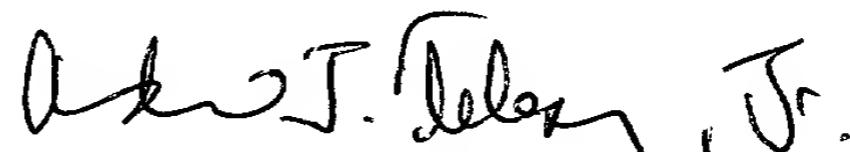
Figures 2-4 have been denoted as "Prior Art".

In order to expedite prosecution of this application, three (3) sheets of formal drawings incorporating the above-noted corrections are enclosed.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 50-0238 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

JONES VOLENTINE, L.L.C.



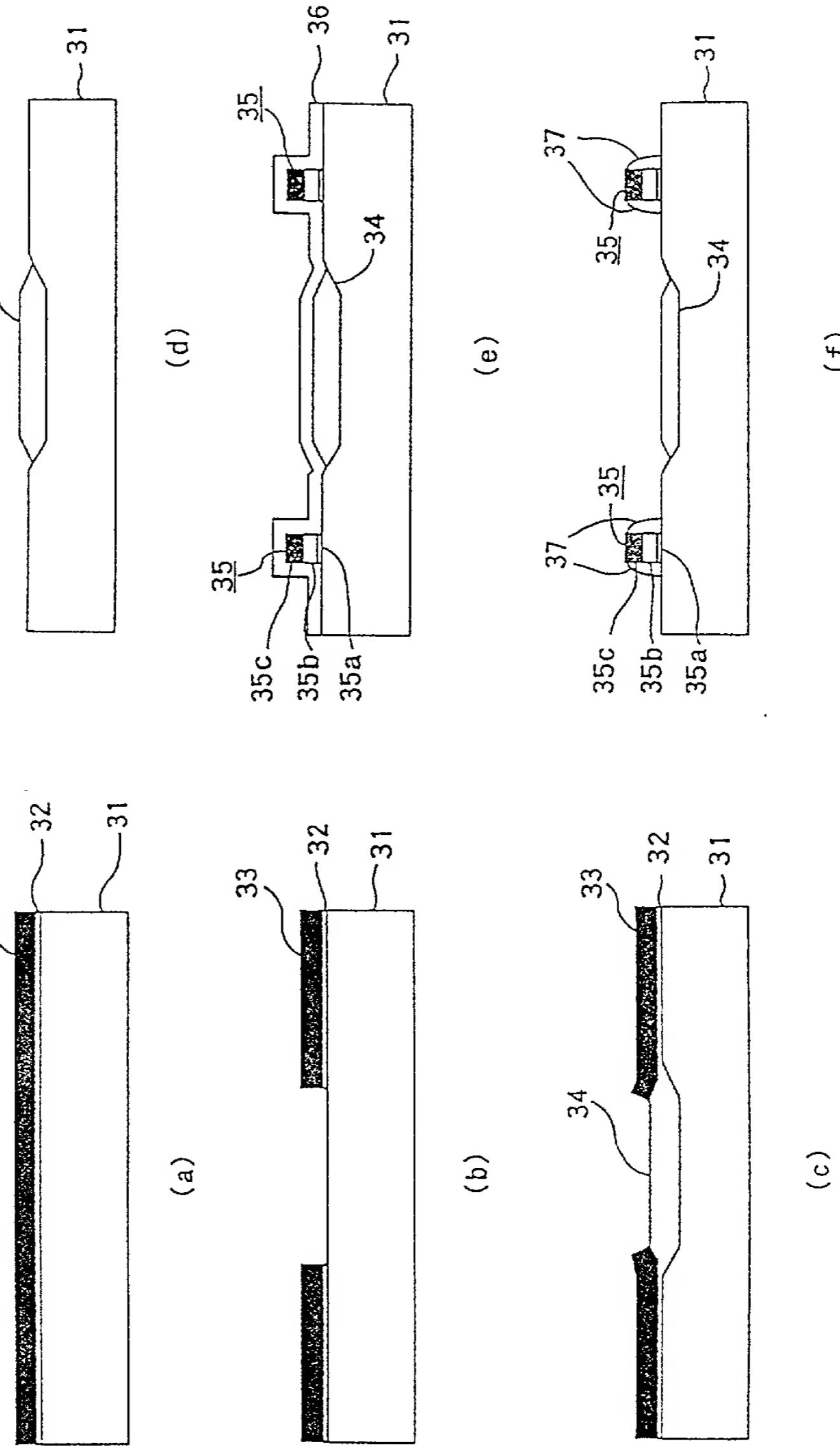
Andrew J. Telesz, Jr.
Registration No. 33,581

AJT:cej

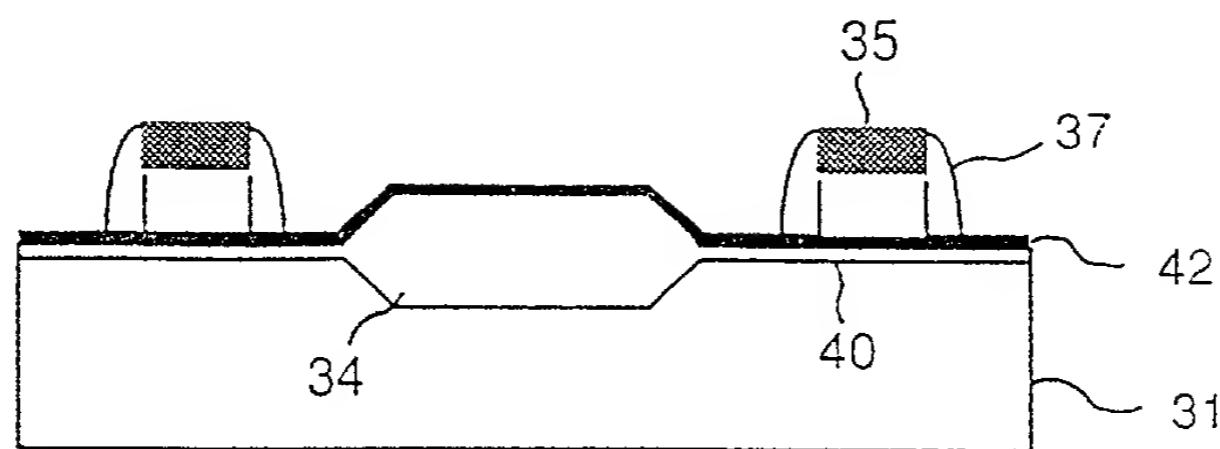
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Reston, VA 20191
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Tel. Fax. (703) 715-0877

Enclosures: Two (2) sheets of red ink drawings
Three (3) sheets formal drawings

[Fig. 2]
PRIOR ART



[Fig. 3]
PRIOR ART



[Fig. 4]
PRIOR ART

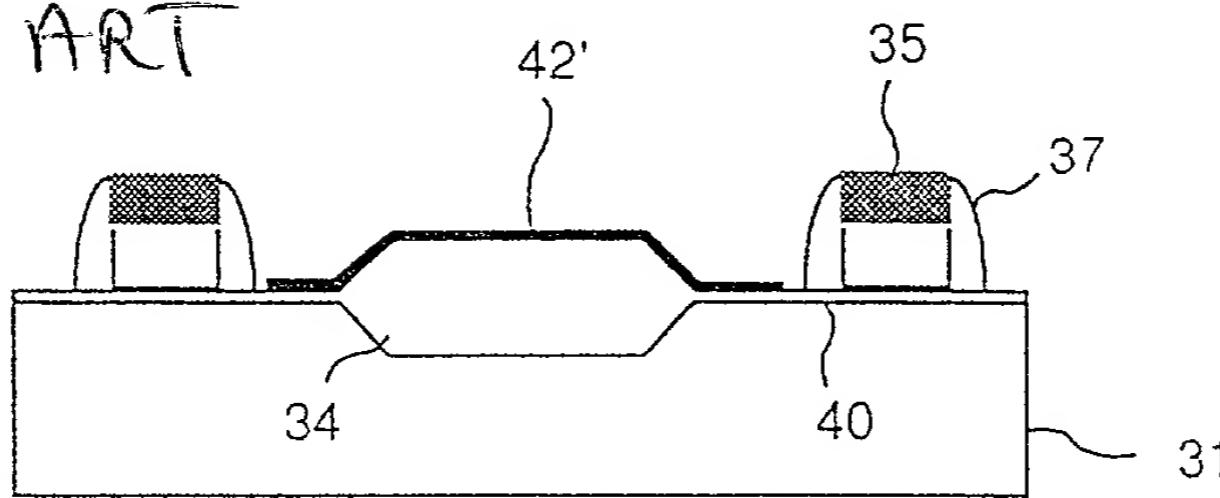


FIG. 1

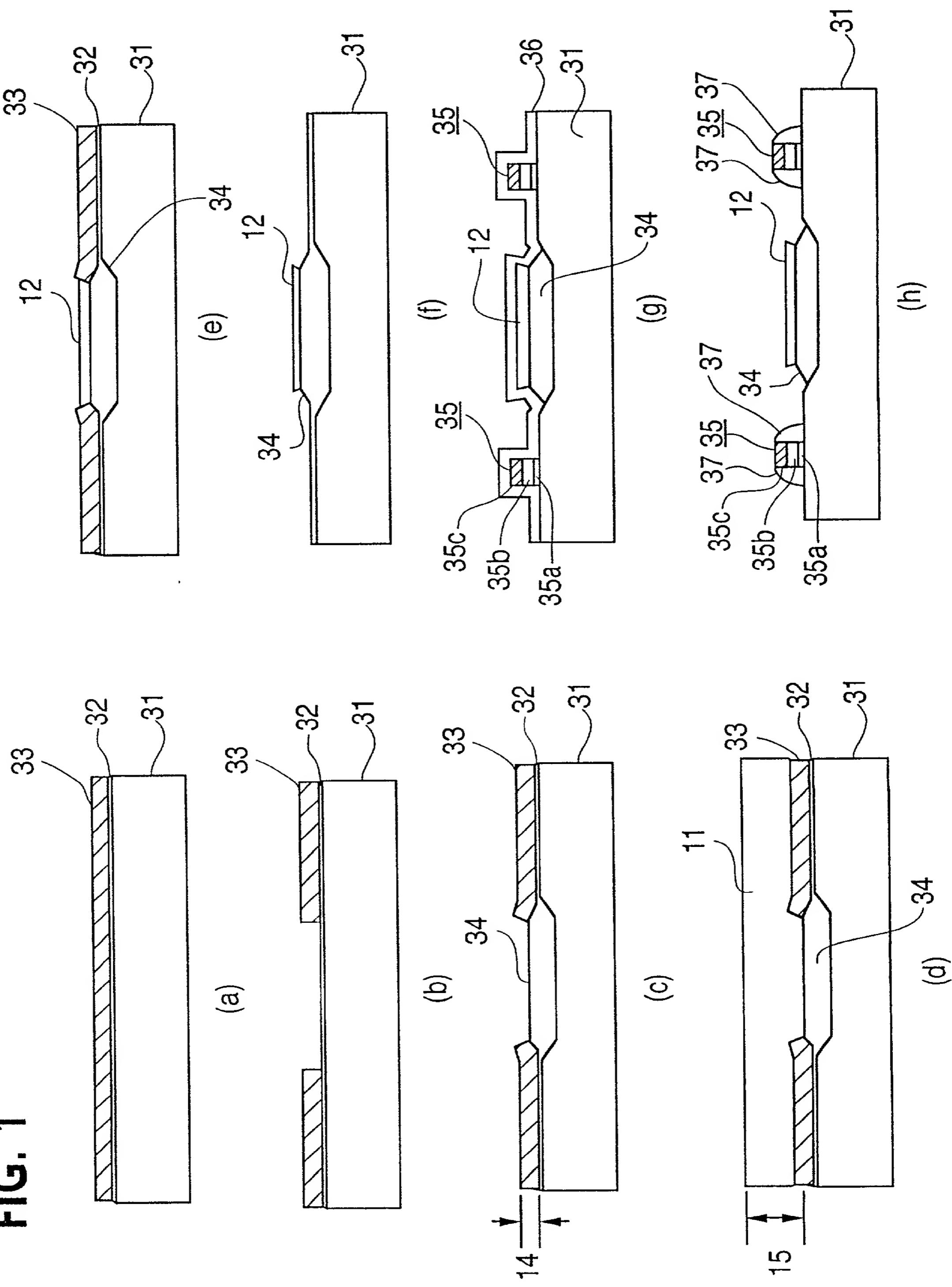


FIG. 2
(PRIOR ART)

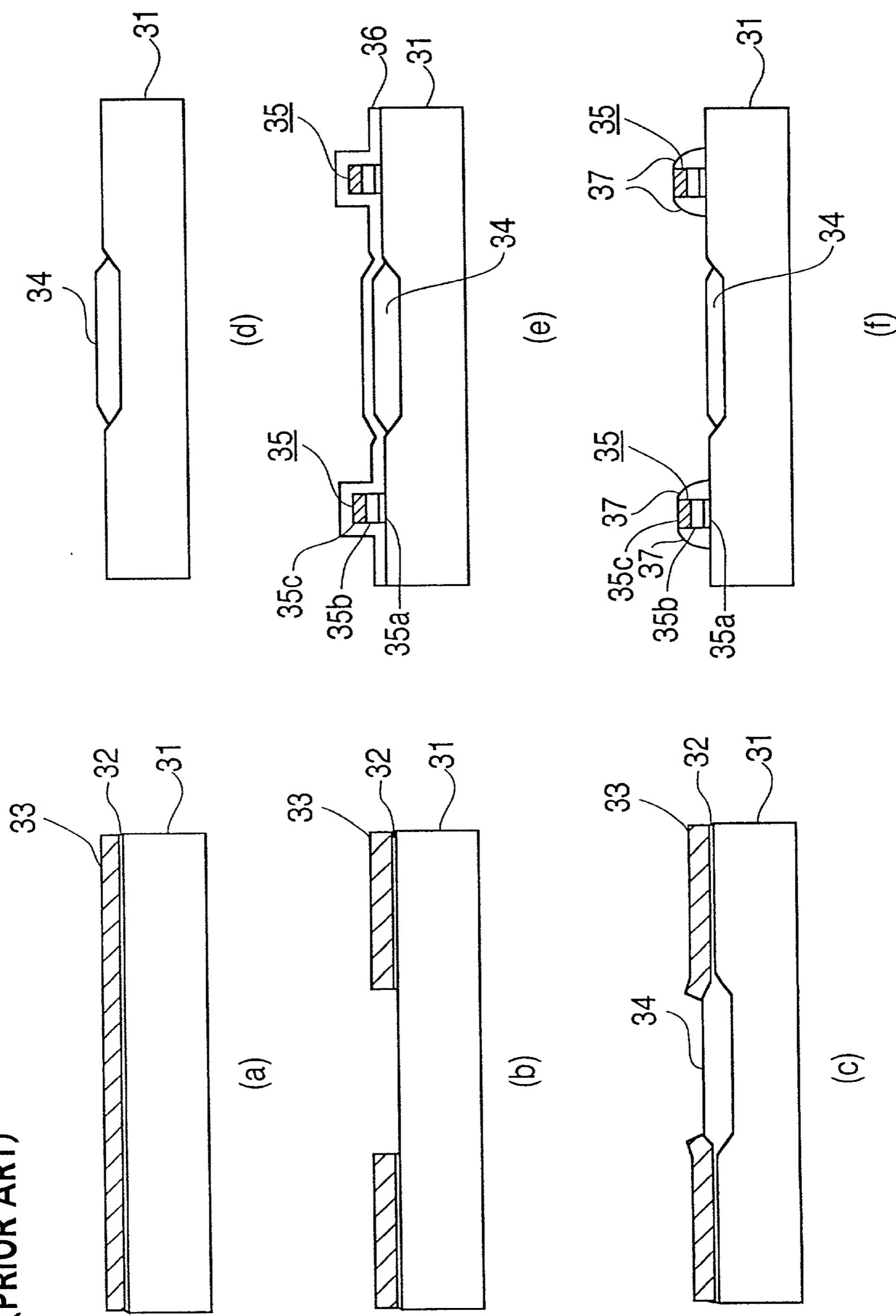


FIG. 3
(PRIOR ART)

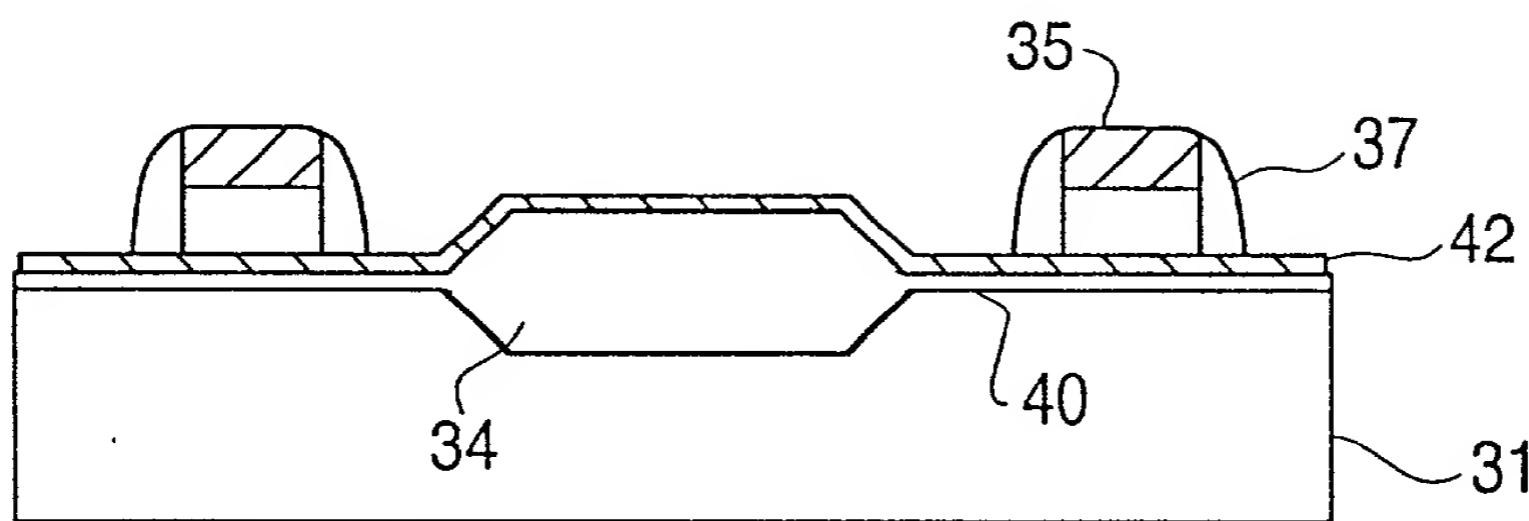


FIG. 4
(PRIOR ART)

